



PATENT
Attorney Docket No.: SONY-14500

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Bruce A. Fairman

Serial No.: 09/708,114

Filed: November 6, 2000

For: **PROGRAMMABLE FIRST-IN
FIRST-OUT (FIFO) MEMORY
BUFFER FOR CONCURRENT
DATA STREAM HANDLING**

Group Art Unit: 2184

Examiner:

**INFORMATION DISCLOSURE
STATEMENT**

260 Sheridan Avenue, Suite 420
Palo Alto, California 94306
(650)833-0160

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SEP 18 2001

Assistant Commissioner for Patents
Washington, D.C. 20231

Technology Center 2100

Sir:

The citations listed below, copies attached, may be material to the examination of the above-identified application, and are therefore submitted in compliance with the duty of disclosure defined in 37 C.F.R. §§ 1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

Applicant has become aware of the following printed publication which may be material to the examination of this application:

- U.S. Patent No. 3,836,722;
- U.S. Patent No. 3,906,484;
- U.S. Patent No. 4,218,756;
- U.S. Patent No. 4,379,294;
- U.S. Patent No. 4,395,710;
- U.S. Patent No. 4,409,656;
- U.S. Patent No. 4,493,021;
- U.S. Patent No. 4,633,392;
- U.S. Patent No. 4,641,238;
- U.S. Patent No. 4,641,307;

not sample

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CERTIFICATE OF MAILING (37 CFR § 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Assistant Commissioner for Patents, Washington D.C. 20231

HAVERSTOCK & OWENS LLP.

Date: 8/24/01 By: James D. Haverstock